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UNITED STATES PATENT AND TRADEMARK OFFICE

IN RE APPLICATION OF:

**Fong Piau and Ong Chun Huat**

SERIAL NUMBER: 10/053,496

FILED: November 9, 2001

FOR: **A Method and System For  
Controlling Compact Flash Memory**

PATENT

ART UNIT NO.: 2818

EXAMINER: UNKNOWN

ATTORNEY DOCKET NO.:  
FLEX1814

San Jose, California  
April 12, 2002

I hereby certify that this document referred to as enclosed therein  
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April, 2002 in an envelope addressed to Box Missing Parts  
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To the Honorable Commissioner of Patents and Trademarks:

Responsive to the Notice to File Missing Parts of Application - Filing Date

Granted, of November 9, 2001, please enter the following Response.

Enclosed for the Commissioner's review are:

1. Copy of the Notice to File Missing Parts of Application - Filing Date

Granted;

2. Oath and Declaration in this matter;
3. Original and copy of Transmittal of Assignment with coversheet;

**RESPONSE OF MISSING PARTS  
ATTORNEY DOCKET NO.: FLEX1814**

**April 12, 2002**

4. Figs. 1 and 2 which were inadvertently omitted from the original filing of the patent application, and as such, are not considered new subject matter and therefore the original filing date of November 9, 2001 for this application be retained.

Fig. 1 is a block diagram used to depict a prior art flash memory controller that can be found in the Description of Related Art section of the original and currently filed patent application (No. 10/053,496).

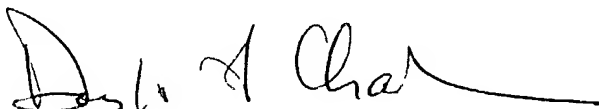
Fig. 2 is a block diagram illustrating the operative components of a compact flash controller in accordance with the present invention as discussed and claimed in the Detailed Description of the Present Invention of the originally filed patent application (No. 10/053,496).

5. Fig. 3H and Fig. 3I, will be corrected as part of either a preliminary amendment or in response to an office action.

6. Check in the amount of \$511.00 for the surcharge fee pursuant to 37 CFR 1.16(e):

Respectfully submitted,

**PENINSULA IP GROUP**  
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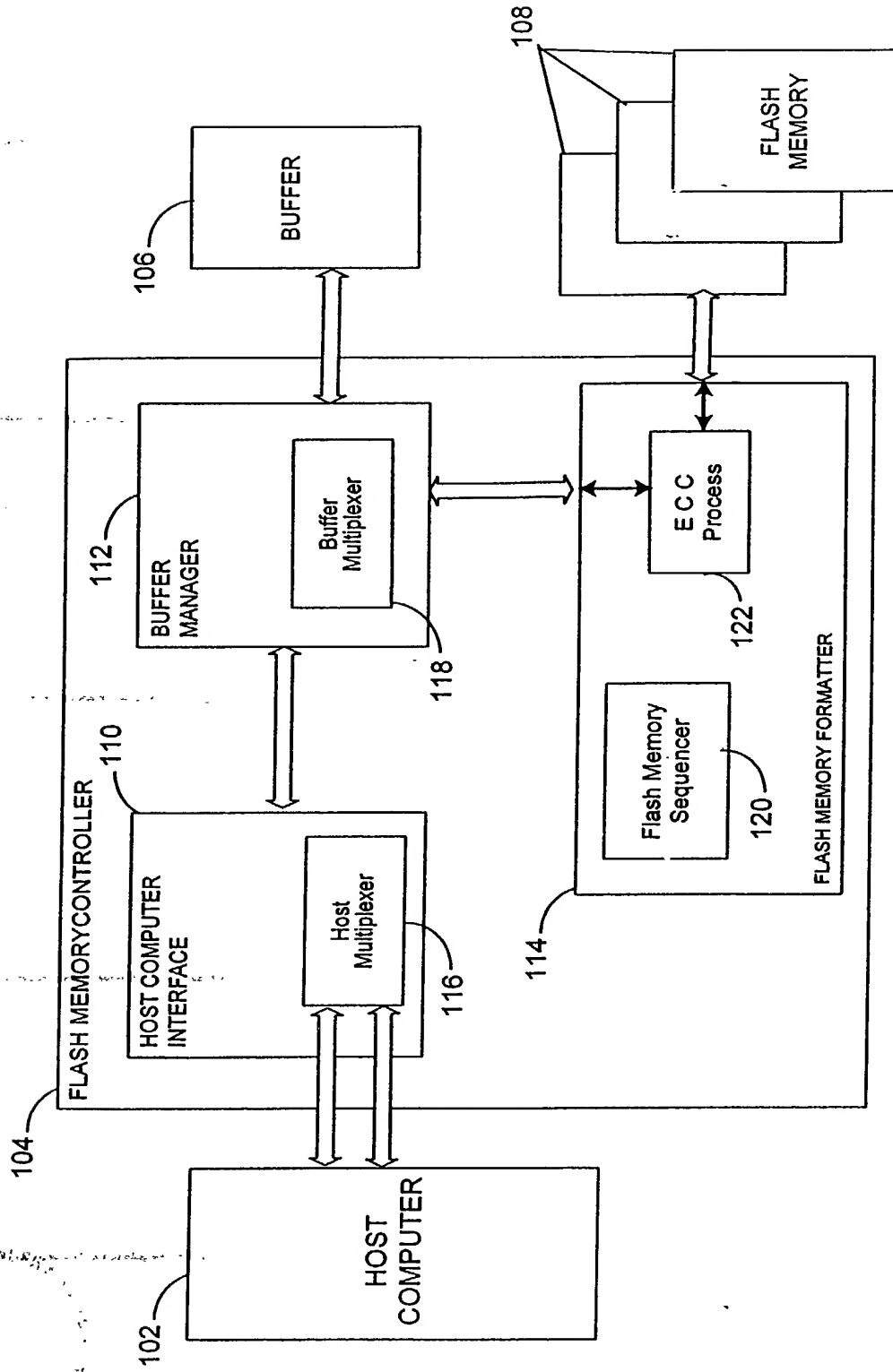


FIG. 1  
(PRIOR ART)

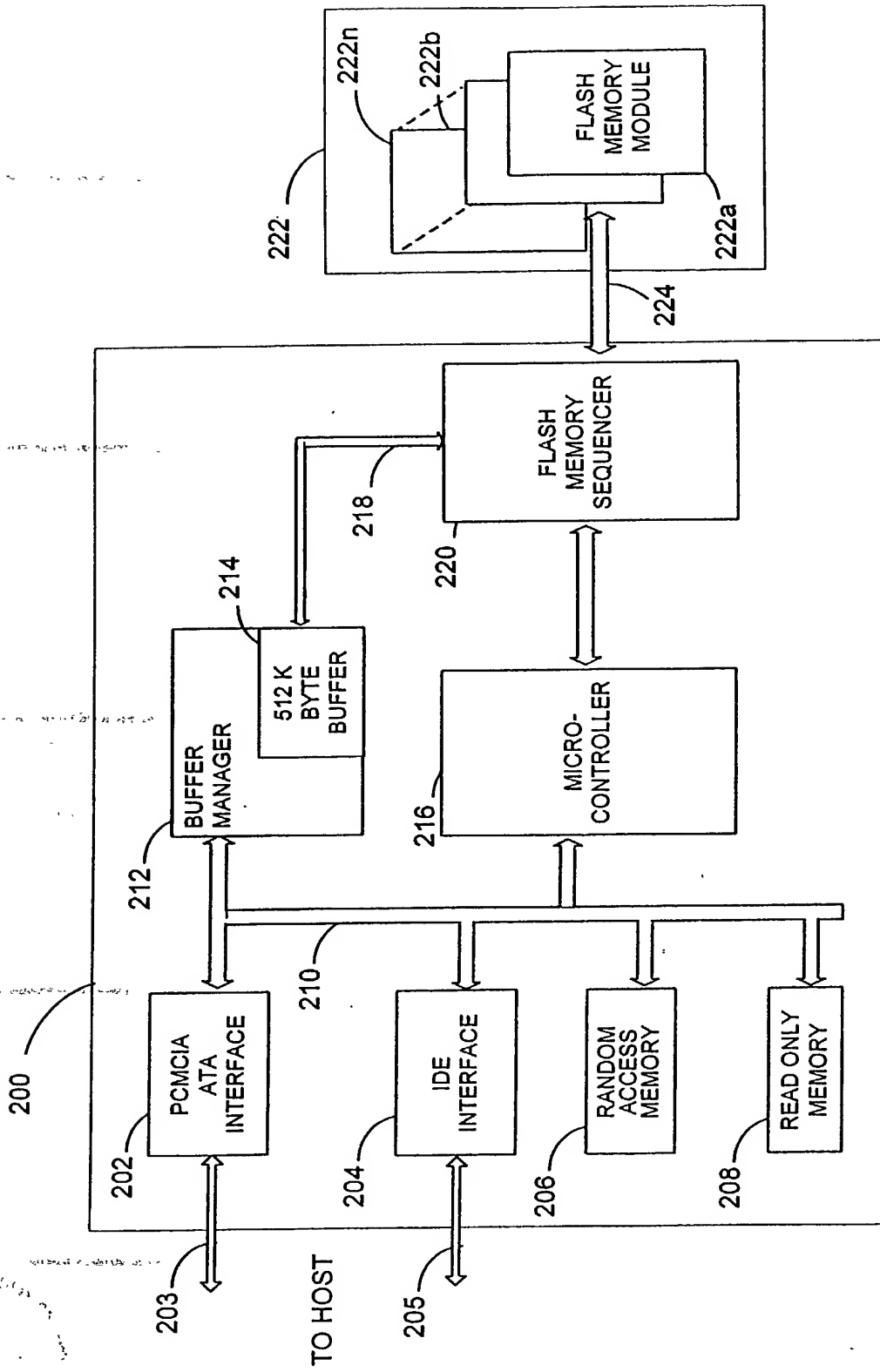


FIG 2